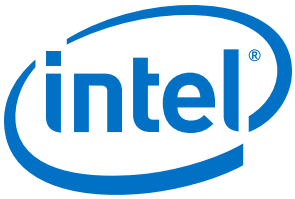
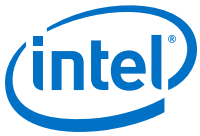


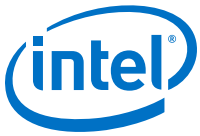
Intel® Communications Chipset 89xx Series Software for Linux*

Getting Started Guide

March 2016

Order No.: 330750-005

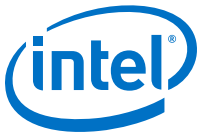


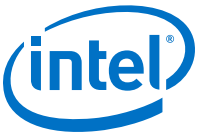


Revision History

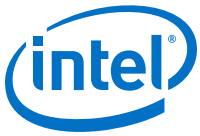
Date	Revision	Description
March 2016	005	Corresponds with software release 2.6. Updates include: • Updated Additional Information on Software on page 7 • Updated Updating Grub Configuration File on page 12
August 2015	004	Corresponds with software release 2.5. Updates include: • Updated Acceleration Software Installation Script on page 15 • Updated Installing the Acceleration Software on page 20
February 2015	003	Corresponds with software release 2.3.0. Updates include: • Changed installation options in Acceleration Software Installation Script on page 15 • Updated Command Line Arguments on page 18, Cross-Compilation Capabilities for Intel® QuickAssist Technology on page 22, and Compiling the Acceleration Sample Code on page 27
November 2014	002	Corresponds with software release 2.2.0. Updates include: • Fixed paths in manual build instructions. • Added "bacomp" command line and compression-build-related note to Command Line Arguments on page 18
July 2014	001	Corresponds with software release 1.1. Updates include: • First “public” version of the document. Based on “Intel Confidential”document number 523128-1.2, with the revision history of that document retained for reference purposes. • Renamed document (was *Intel® Communications Chipset 8925 to 8955 Series Software for Linux* Getting Started Guide*). • Added information for mux package. • Removed some platform-specific content.
November 2013	1.2	Corresponds with software release 1.0.1. Updates include: • Changed document and software title (expanded SKU range to include "8955")
November 2013	1.1	Corresponds with software release 1.0. Updates include: • Modified Step 3 in Unpacking the Software on page 15 • Modified Step 5 and added new information and note to Step 18 in Installing the Hard Disk IDE Patch
October 2013	1.0	Corresponds with software release 1.0. Updates include: • Added additional debugging information to Installing the Acceleration Software on page 20 and signOfLife Tests on page 30. • Changed product branding.
June 2013	0.5	Initial release of document.

Contents





Tables



1.0 Introduction

1.1 About this Manual

This Getting Started Guide documents the instructions to obtain, build, install and exercise the Intel® Communications Chipset 89xx Series Software package. Additionally, this document includes brief instructions on configuring the supported development board.

Note: The software described in this document relates to a platform that pairs the Intel® Communications Chipset 8900 to 8920 Series and/or the Intel® Communications Chipset 8925 to 8955 Series with Intel® Xeon® Processors.

In this document, for convenience:

- *Software package* is used as a generic term for the Intel® Communications Chipset 89xx Series Software package.
- *Acceleration drivers* is used as a generic term for the software that allows the QuickAssist Software Library APIs to access the Intel® QuickAssist Accelerator(s) integrated in the Intel® Communications Chipset 8900 to 8920 Series and Intel® Communications Chipset 8925 to 8955 Series.

The document is organized as follows:

- Installing the Operating System on page 11
- Building and Installing Software on page 15
- Sample Applications on page 27
- Glossary on page 35

1.2 Additional Information on Software

The software release package for Linux* has been validated with CentOS 7 and Fedora 16.

1.2.1 Where to Find Current Software and Documentation

The software release and associated collateral can be found on the open source website: https://01.org/packet-processing/intel%C2%AE-quickassist-technology-drivers-and-patches.

See Product Documentation on page 8 for a list of associated collateral.

1.2.2 Product Documentation

Table 1 on page 8 lists the documentation supporting this release. All documents can be accessed as described in Where to Find Current Software and Documentation on page 7.

Table 1. Product Documentation and Software

Title	Number
Intel® Communications Chipset 89xx Series Software for Linux Getting Started Guide* (this document)	330750
Intel® Communications Chipset 8900 to 8920 Series Software Programmer's Guide	330753
Intel® Communications Chipset 8925 to 8955 Series Software Programmer's Guide	330751
Intel® QuickAssist Technology Software Release Notes	330683
Intel® QuickAssist Technology API Programmer's Guide	330684
Intel® QuickAssist Technology Cryptographic API Reference Manual	330685
Intel® QuickAssist Technology Data Compression API Reference Manual	330686
Using Intel® Virtualization Technology (Intel® VT) with Intel® QuickAssist Technology Application Note	330689

1.2.3 Pre-boot Firmware

The latest release of the development board pre-boot firmware (BIOS) is also located on the Intel Business Portal.

Please refer to the Release Notes listed in Table 1 on page 8 for the correct firmware version.

1.3 Related Software and Documentation

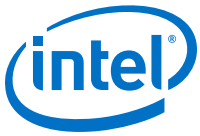
Refer to the Development Kit User's Guide for your hardware for additional information on the development board including board layout, components, connectors, jumpers, headers, power and environmental requirements, and pre-boot firmware.

Please follow the directions in Where to Find Current Software and Documentation on page 7 to locate this collateral.

1.4 Conventions

The following conventions are used in this manual:

- `Courier font` - code examples, command line entries, API names, parameters, filenames, directory paths, and executables
- **Bold** text - graphical user interface entries and buttons
- *Italic* text – key terms and publication titles



1.5 Software Overview

The software is described in the following topics:

- Features Implemented
- List of Files in Release
- Package Release Structure

1.5.1 Features Implemented

Note: For feature details and limitations, if any, refer to the Release Notes.

1.5.2 List of Files in Release

A Bill of Materials (BOM) is included as a text file in the software package(s).

1.5.3 Package Release Structure

After unpacking the tar file, the directory should contain:

Files/Directory	Comments
./QATmux.L.a.b.c-n.tar.gz	Top-level QAT package
./filelist	List of files in this package
./installer.sh	Installer script
./LICENSE.GPL	Licence file
./Versionfile	Version file
./QAT1.5	Directory containing QAT1.5.L.a.b.c-n.tar.gz
./QAT1.6	Directory containing QAT1.6.L.a.b.c-n.tar.gz

Depending on the devices detected on the platform, the installer script will build the appropriate driver(s) and, if more than one driver is then built, it will also build the qat_mux.

The following extra files/directories may be present.

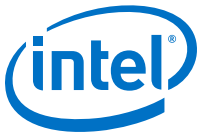
In the top-level folder:

Files/Directory	Comments
./cpa_mux	Directory containing qat_mux software. This will only be present if both drivers are built.
./InstallerLog.txt	Log of installer script output

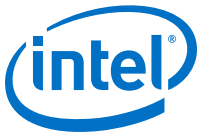
In one or both of the QAT1.x directories:

Files/Directory	Comments
./QAT1.x/QAT1.x.L.a.b.c-n.tar.gz	QAT1.x driver package
./QAT1.x/filelist	List of files in the QAT1.x package
./QAT1.x/build	Build output directory

continued...



./QAT1.x/versionfile	Version of the QAT1.x driver
./QAT1.x/quickassist	Top-level acceleration software directory for QAT1.x driver
./QAT1.x/quickassist/adf	Acceleration Device Framework source files
./QAT1.x/quickassist/build_system	Build system files
./QAT1.x/quickassist/config	Configuration files
./QAT1.x/quickassist/include	Common acceleration software header files
./QAT1.x/quickassist/lookaside	Service Access Layer source files
./QAT1.x/quickassist/utilities	OSAL and Firmware loading source files



2.0 Installing the Operating System

The section describes the process of obtaining, installing, and configuring the operating system (OS) on the development board.

2.1 Acquiring CentOS 7

Note: CentOS* 7 is based on Red Hat Enterprise Linux*7.1 (or later) and may also be referred to as CentOS 7.1. In general, using the latest version of CentOS 6 or CentOS 7 is recommended, though it is possible that changes to the Linux* kernel in the most recent versions may break some functionality for particular releases.

CentOS 7 is a Linux* distribution built on free and open source software. The software package from Intel does not include a distribution of CentOS or any other Linux variant. The software package includes Linux device driver source developed by Intel.

CentOS 7 x86_64 can be obtained from:

https://wiki.centos.org/Download

Note: This document is written with the CentOS 7 DVD Install Media in mind. Using any Live Media versions is not recommended.

2.2 Configure the BIOS

Note: Updating to the latest stable BIOS for your platform is strongly recommended.

If the performance achieved from the acceleration software is not meeting the advertised capability, some BIOS changes might be required:

- Setting links to train to the highest possible speed, e.g., PCIe Gen3 instead of PCIe Gen2 or Gen1
- Disabling some CPU power-saving features.

In the BIOS setup, set the first boot device to be the DVD-ROM drive and the second boot device to be the drive on which CentOS 7 will be installed.

2.3 Installing CentOS 7

For complete additional (and non-standard) CentOS installation instructions, please refer to the online Installation Guide at:

https://wiki.centos.org/HowTos

This section contains basic installation instructions. For the purposes of this Getting Started Guide, it is assumed that the installation is from a DVD image.

Note: If the hard drive already has an operating system, some of the following steps may be slightly different.

1. When the development board starts, it should begin booting from the CentOS 7 installation disc. If not, verify the Boot Order settings described in Configure the BIOS on page 11.
2. At the welcome prompt, select **Test this Media & Install CentOS 7** and click **Enter**.
3. Select **OK** to begin testing the installation media.

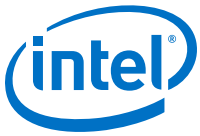
   Note: It is highly recommended that the CentOS 7 installation disc is verified prior to an installation of the OS.
4. After verifying the CentOS 7 installation disc(s), the graphical portion of the installation is loaded. Click **Next** to continue the installation process.
5. Update **DATE & TIME** options if required, including the time zone, network time (if desired).
6. **SOFTWARE SELECTION**. For the best evaluation experience and to avoid any build issues with the acceleration software package, it is strongly recommended to select "Development and Creative Workstation" as the "Base Environment". Select the following "Add-Ons for Selected Environment" as well: "Additional Development", "Development Tools", and "Platform Development". Also select "Virtualization Hypervisor" if virtualization is required (and supported by the acceleration software package).
7. Select **INSTALLATION DESTINATION**. If the correct target device is not selected, select it. Click **Done**.
8. Select **NETWORK & HOST NAME**. In most cases, changing the Ethernet connection from"OFF" to "ON" will be desired. Also set the Host name, if required. Click **Done**.
9. Once all items on the **INSTALLATION SUMMARY** are configured, select **Begin Installation**.
10. Set the root password and create a user. When creating a user, select "Make this user administrator" to enable sudo. Select **Done** and wait for the installation to complete.
11. When the installation completes, the install DVD should be ejected. Remove the DVD and select **Reboot** when prompted.
12. When the installation completes, continue with Updating Grub Configuration File on page 12.

2.4 Updating Grub Configuration File

This section contains instructions on updating the grub configuration file.

Note: Root access is required in order to make grub changes.

If the acceleration software will be used with a virtualized (SRIOV) environment (if supported by the acceleration software package), update grub to add intel_iommu=on to the boot options, using the following guide:



https://access.redhat.com/documentation/en-US/Red_Hat_Enterprise_Linux/7/html/System_Administrators_Guide/ch-Working_with_the_GRUB_2_Boot_Loader.html

Consult the following document for more information on using the acceleration software in a virtualized environment: *Using Intel® Virtualization Technology (Intel® VT) with Intel® QuickAssist Technology Application Note* (see Product Documentation on page 8).

2.5 Configuring Linux

Once the operating system is installed, there are a few configuration items that may need to be completed, such as updating the yum configuration files and enabling the development platform to operate within a corporate network. This section describes these items.

2.5.1 Updating yum Configuration Files

yum is an application that can be used to perform operating system updates. In order to use yum in a corporate network, the following change may be required.

2.5.1.1 /etc/yum.conf

If the system needs to connect to internet through a corporate firewall, yum needs to be updated to use the proxy server. Add a line similar to the following in the `/etc/yum.conf` file. The line can be added to the end of the file. Contact your network administrator for details on the proxy server.

```
proxy=http://<proxy_server:portnum>
```

where `<proxy_server:portnum>` is replaced with your server information.

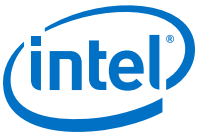
2.6 System Security Considerations

This section contains a high-level list of system security topics. Specific OS/filesystem topics are outside of the scope of this document. For more information, see the *Programmer's Guide* for your platform, specifically the *Secure Architecture Considerations* section.

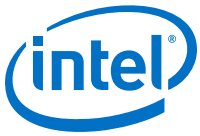
Securing your operating system is critical. You should consider the following items:

Note: This is not an exhaustive list.

- Employing effective security policies and tools; for instance, SELinux is configured correctly and is active
- Running and configuring the firewall(s)
- Preventing privilege escalation at boot (including recovery mode); for instance, setting a grub password. Additional details are described below.
- Removing unnecessary software packages
- Patching software in a timely manner
- Monitoring the system and the network
- Configuring and disabling (as appropriate) remote access



- Disabling network boot
- Requiring secure passwords
- Encrypting files, up to full-disk encryption
- Ensuring physical security of the system and the network
- Using mlock to prevent swapping sensitive variables from RAM to disk
- Zeroing out sensitive variables in RAM



3.0 Building and Installing Software

This chapter provides details on building and installing the software on the development kit.

3.1 Unpacking the Software

The software package comes in the form of a tarball. See Where to Find Current Software and Documentation for the software location.

The instructions in this document assume that you have super user privileges.

```
# su
<enter password for root>
```

1. Create a working directory for the software. This directory can be user defined, but for the purposes of this document, a recommendation is provided.

   ```
   # mkdir /QAT
   # cd /QAT
   ```

   Note: In this document, the working directory is assumed to be `/QAT`.

2. Transfer the tarball to the development board using any preferred method, for example USB memory stick, CDROM, or network transfer in the `/QAT` directory. Unpack the tarball using the following command:

   ```
   # tar -zxof <tarball name>
   ```

3. Restricting access to the files is recommended:

   ```
   # chmod -R 770 /QAT
   ```

Result: The package is unpacked and the installation script and other items are created in the `/QAT` directory. See Package Release Structure on page 9.

3.2 Acceleration Software Installation Script

An installation script is provided that walks you through building/installing the software. Refer to Installation on a New Development Platform for instructions on installing the software on a new platform.

The installation script can also be launched with command line arguments giving the option to bypass the interactive setup. Refer to Command Line Arguments for additional information.

For details on minimizing Acceleration Software compilation time, refer to Minimizing Acceleration Software Compilation Time on page 26.

The *Programmer's Guide* for your platform describes required and optional build flags in the *Build Flag Summary* section. If you are using the installation script, the required build flags are handled by default. The optional build flags control the driver functionality and can be used with the installation script or the command line arguments.

The installation script file must be run as root. If the script is executed as a user other than root, the following error is returned: `ERROR This script must be run as root.`

Launch the script using the following command:

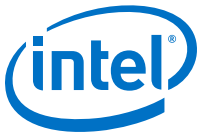
```
# ./installer.sh
```

A welcome message is displayed, followed by Installation Options. The table below lists some of the available installation options.

Note: If you have not unpacked the release package tarball as described in Unpacking the Software on page 15, errors will be returned.

Table 2. Installation Options

Option	Name	Description
1	Build	Builds the acceleration software/sample code mentioned in the Configuration. The software is not installed.
2	Clean Build	Cleans the previously built software.
3	Install	Installs the acceleration software/sample code mentioned in the Configuration.
4	Uninstall	Uninstalls the software.
5	Show Accel Info	Displays the number of chipset devices available on the system and the B:D:F for each device.
6	Change Configuration	Option to change the configuration to another combination. There is a sub-menu that allows you to select which software components are built/installed.
0	Exit	Exit the installation script.



Note: When you run the installer, it also shows Intel® QuickAssist Technology devices found on the system and default Configuration to build/install software components. For example, after issuing the command `#./installer.sh`, output similar to the following should appear:

```
============================================================
Welcome to Intel(R) QuickAssist Interactive Installer -v2
============================================================


 Please Select Option :
 ----------------------
 1   Build
 2   Clean Build
 3   Install
 4   Uninstall
 5   Show Accel Info
 6   Change Configuration
 7   Dependency List
 0   Exit


 QAT Devices found: 1 QAT1.6 devices
                    0 QAT1.5 devices
 Configuration:     Build Acceleration and Sample Code
                    for QAT1.6
                    And SR-IOV Disabled
 Exit and re-enter to set defaults
```

Note: To select different software components in the configuration section, use option 6. The following are the available options to change the configuration:

```
  Options to change configuration:
  --------------------------------
  a1  Set Build Target as "sample_code only"
  a2  Set Build Target as "DC_ONLY acceleration and sample_code"
  b1  Set Build Location
  c1  QAT1.5 Only
  c2  QAT1.6 Only
  c3  QAT1.6 with Mux
  c4  QAT1.5 and QAT1.6 with Mux
  c5  Enable/Disable Mux based on devices detected now
  d1  Set SRIOV Mode to "Host"
  d2  Set SRIOV Mode to "Guest"
  e1  Toggle GigE Watchdog for Acceleration Install (Disabled)
  z1  Go Back to Main Menu
```

Note: To reset to the default configuration, you may need to exit and launch the installation script again.

Note: The interactive installer.sh does not handle all options that may be of interest. For instance, there are a wide range of configurations that are possible, including build or install, with or without support for multiple acceleration hardware generations (i.e., using the "mux" layer), virtualization support (host or guest) or no virtualization support. Some modifications to the installer.sh may be required.

Some packages may have slightly different options than those listed above.

On recent Linux* kernels, there is an upstreamed version of the Intel® QuickAssist Technology driver, and it will interfere with the loading of the driver included with the software package assumed in this document. The qat_service accounts for this by removing the upstreamed kernel modules, but if qat_service is not used, the following error may be seen when trying load the driver:

```
icp_qa_al err: icp_adf_subsystemRegister: Failed to get accel
head.
```

To start the driver, use the qat_service, or rmmod the upstreamed modules (`qat_dh895xcc, intel_qat, icp_qa_al`) and insert the modules built with the software package assumed in this document before starting the driver.

3.2.1 InstallerLog

The `InstallerLog.txt` file is appended after each installation with the time/date and the output of the build/install. If any issues were seen during the installation, check the log file for details.

3.2.2 Command Line Arguments

The *Programmer's Guide* for your platform describes required and optional build flags in the *Build Flag Summary* section. If you are using the installation script, the required build flags are handled by default. The optional build flags control the driver functionality and can be used with the installation script or the command line arguments.

Note: If the kernel source is not at location "`/usr/src/kernels/`uname -r`", then export the kernel source path to an appropriate location:

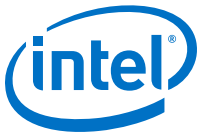
```
export KERNEL_SOURCE_ROOT=<path_to_kernel_source>
```

Use the installer to help to select available options: `#./installer.sh help`

Usage: `./installer.sh <options>`

You may choose options from the following groups:

- actions: `build / install / clean / uninstall / version / help`
- driver_options: `QAT1.5 / QAT1.6 / mux / QAT1.6_mux`
- sriov_options: `host / guest`
- service_options: `build_sample / build_dc_only / gige`
- path: Location where you would like to build the Intel® QuickAssist Technology package



Note: Do not combine action and service options, and be sure to start your path with /

Example Usage:

- To install QAT1.6 acceleration with Mux `./installer.sh install QAT1.6_mux`
- To install QAT1.5 acceleration in host `./installer.sh install QAT1.5 host`

Refer to "`./installer help`" output for details.

Note: Not all command line options are supported by every software package.

3.3 Installation on a New Development Platform

This section walks you through the installation of the software on a new development platform.

Note: If you require SATA IDE support, you need to install the Hard Disk IDE Patch.

Note: If you are updating from a previous release of the software, you may wish to identify the changes between the releases. See Updating the Acceleration Driver from a Previous Release on page 24 for additional information.

Note: One Acre Ethernet cards that are functional on Shumway CRBs containing Intel® Communications Chipset 8900 to 8920 Series devices are not functional on Shumway CRBs containing Intel® Communications Chipset 8925 to 8955 Series devices. A separate Ethernet card is required for network connectivity. Refer to Updating yum Configuration Files on page 13 for additional configuration information.

3.4 Starting/Stopping the Acceleration Software

When the Acceleration software is installed, a script file titled `qat_service` is installed in the `/etc/init.d` directory.

The script file can be used to start and stop the Acceleration software. To start the software, issue the following commands:

```
# service qat_service start
```

Notes: If the `service qat_service start` command fails, verify the following:

- Software is installed.
- Acceleration software is already running.
- If you are not using virtualization, verify the Kernel option `intel_iommu=off` has been configured as specified in Updating Grub Configuration File on page 12.
- Verify the device is enumerated properly using the `lspci` command described in Installing the Acceleration Software on page 20.

To stop the software, issue the following command:

```
# service qat_service stop
```

To stop the software and remove the kernel driver, issue the following command:

```
# service qat_service shutdown
```

When the Acceleration software is installed, it is set to load automatically when the operating system loads.

Note: If the following error message is returned: `icp_qa_al err: adf_aeUcodeMap: Mapping of Firmware failed, status=0xa116 "UOF is incompatible with the chip type/revision"` you have attempted to install the software package on a system without the latest chipset device silicon.

3.5 Installing the Acceleration Software

When installing acceleration software on a system that had a previous or modified version of the acceleration software installed, it is strongly recommended to uninstall the previous acceleration software first, using the installer.sh script in the previous acceleration software package.

1. Power on the system and proceed with the instructions below.
2. Open a Terminal Window and switch to superuser.

```
# su
<enter root password>
```

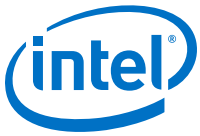
3. In the `/QAT` directory, start the installation script.

```
# /QAT
# ./installer.sh
```

Select the **Install Acceleration** installation option. This builds and installs the Acceleration software. You will be prompted for a directory location to build the package and the Build Output Directory. Use the default values provided by the installation script.

Note: `$ICP_ROOT` is automatically set by `installer.sh` and can vary during the installer run, depending on which driver is being built, i.e., it is set to `/QAT/QAT1.x/` during the build of each `QAT1.x` driver.

Note: If `Error: Could not open file: /etc/dh895xcc_qa_dev0.conf` is shown during the Accleration installation, it is possible that the configuration files were not copied from `$ICP_ROOT/quickassist/config` due to the way that `installer.sh` detects the acceleration device(s) via `lspci`. Remove the line from `/usr/share/hwdata/pci.ids` that contains the string `0435` and rerun the Acceleration installation. Alternatively, copy the appropriate configuration files from `$ICP_ROOT/quickassist/config` to `/etc` and then restart the service with `service qat_service restart`.



Note: If a `failed to start device` error is shown during the Acceleration installation, ensure that the kernel option `intel_iommu=off` has been configured as specified in Updating Grub Configuration File on page 12. If this kernel parameter is not specified, acceleration services are only available in a guest operating system.

Note: If **Software Development** was not selected during the OS install, you may see an error message similar to the following during the acceleration install:

```
"make: *** /lib/modules/3.1.0-7.fc16.x86_64/build/: No such file or
directory. Stop."
```

Reinstall the OS and select the **Software Development** option as described in Installing CentOS 7 on page 11, or run the following commands:

```
# yum -y install kernel-devel-$(uname -r)
# yum -y install gcc
# yum -y install zlib-devel
# yum -y install openssl-devel
```

Note: If an error like `cpa_mux: exports duplicate symbol cpaCyBufferListGetMetaSize` (owned by icp_qa_al) occurs, a previous version of the driver is still loaded. Run installer.sh for that package and Select Uninstall.

4. During the installation, the following message is displayed:

```
*** No error detected in InstallerLog.txt file ***
```

At the end of the installation, the following messages are displayed:

```
*** Acceleration Installation Complete ***
```

Refer to the `InstallerLog.txt` file for additional detail on the installation. It is also a good idea to check `/var/log/messages` or `dmesg` to make sure that the acceleration service started. Warning messages related to `Invalid core affinity` can be addressed by modifying the configuration files so that no core numbers are referenced beyond the core count of the system. See Configuration Files on page 24 for more detail.

Note: After building/installing the Acceleration Software, it is highly recommended to secure the build output files (the files located in `$ICP_ROOT\build`) by either deleting them or setting permissions according to your needs.

5. Use the **0** option to exit the installation.
6. After installing the Acceleration Software, it is recommended to verify that the acceleration software kernel object is loaded and ready to use. Depending on which drivers are loaded, the objects built have different names (see the following table).

Table 3. Build Output Objects

Case	Kernel object(s)	User space object(s)	Static libraries
QAT1.5 only installed	icp_qa_al.ko	libicp_qa_al_s.so	libicp_qa_al.a
QAT1.6 only installed	icp_qa_al.ko	libicp_qa_al_s.so	libicp_qa_al.a
Mux case, i.e., both QAT1.5 and QAT1.6 installed	qat_1_5_mux.ko qat_1_6_mux.ko qat_mux.ko	libqat_1_5_mux_s.so libqat_1_6_mux_s.so libqat_mux_s.so	libqat_1_5_mux.a libqat_1_6_mux.a libqat_mux.a

The following operations can be used to verify that the correct kernel objects are loaded according to the above table:

```
# lsmod | grep qa
```

See section, "Support for Multiple Acceleration Hardware Generations", in the *Intel® Communications Chipset 8925 to 8955 Series Software Programmer's Guide* for more information.

If `icp_qa_al` is not returned, then the acceleration software is not installed and is not ready for use. Refer to the `Installer.log` file in the `/QAT` directory for additional information. If necessary, run the installation script again and select **Install Acceleration**.

Post-requisites: Once the installation/building is complete, proceed to Sample Applications to execute applications that exercise the software.

3.6 Cross-Compilation Capabilities for Intel® QuickAssist Technology

These capabilities enable users to compile the Intel® QuickAssist Technology driver on one system for the targeted architecture of another system.

If you are cross-compiling for the first time, you need to install the following packages:

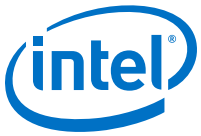
- `yum -y install glibc-devel.i686`
- `yum -y install openssl-devel.i686`
- `yum -y install libgcc.i686 --setopt=protected_multilib=false`
- `cp /lib/libz.so.1.2.5 /usr/lib/libz.so`

If cross-compiling for both `QAT1.x` drivers and the QATmux, then:

```
# export WITH_CPA_MUX=1
```

Use the following commands to cross-compile each `QAT1.x` driver:

```
# export ICP_ROOT=<QATdir>
```



where <QATdir> is /QAT/QAT1.6 or /QAT/QAT1.5, depending on what hardware support is required. If building with mux support, always use the QAT1.6 sample code.

```
# cd quickassist
# export ICP_ENV_DIR=$ICP_ROOT/quickassist/build_system/build_files/env_files
# export ICP_BUILDSYSTEM_PATH=$ICP_ROOT/quickassist/build_system
# export ICP_BUILD_OUTPUT=$ICP_ROOT/build
# export ICP_TOOLS_TARGET=accelcomp
# export LD_LIBRARY_PATH=$ICP_ROOT/build
For i386 machines:
# make ICP_ARCH_USER=i386
For i686 machines:
# make ICP_ARCH_USER=i686
```

Next, go to the build folder and enter:

```
# cd ../build
# file *
```

Here's a sample output for a non-mux build (see Package Release Structure on page 9 for file names in mux build cases):

```
adf_ctl:                          ELF 32-bit LSB executable, Intel 80386, version 1 (SYSV),
dynamically linked (uses shared libs), for GNU/Linux 2.6.32, not stripped
c2xxx_qa_dev0.conf:               ASCII English text
dh89xxcc_qa_dev0.conf:            ASCII English text
dh89xxcc_qa_dev0_single_accel.conf: ASCII English text
dh89xxcc_qa_dev1.conf:            ASCII English text
gige_watchdog_service:            Bourne-Again shell script, ASCII text executable
icp_gige_watchdog:                ELF 32-bit LSB executable, Intel 80386, version 1 (SYSV),
dynamically linked (uses shared libs), for GNU/Linux 2.6.32, not stripped
icp_qa_al.ko:                     ELF 64-bit LSB relocatable, x86-64, version 1 (SYSV), not
stripped
libadf_proxy.a:                   current ar archive
libicp_qa_al.a:                   current ar archive
libicp_qa_al_s.so:                ELF 32-bit LSB shared object, Intel 80386, version 1 (SYSV),
dynamically linked, not stripped
libosal.a:                        current ar archive
mmp_firmware.bin:                 data
mmp_firmware_c2xxx.bin:           data
mof_firmware.bin:                 data
mof_firmware_c2xxx.bin:           data
qat_service:                      Bourne-Again shell script, ASCII text executable
```

As you can see, the user files, `adf_ctl` and `libicp_qa_al_s.so`, are 32-bit files while `icp_qa_al.ko` is a 64-bit file.

Cross-Compiling the Sample Code for 32-Bit User Space

Change directories to the top-level sample code directory:

```
# cd $ICP_ROOT/quickassist/lookaside/access_layer/src/sample_code
```

Compile the user space sample application:

```
For i386 machines:
# make perf_user ARCH=i386
For i686 machines:
# make perf_user ARCH=i686
```

3.7 Configuration Files

When the Acceleration software loads, it is configured based on settings in the platform-specific configuration files. The configuration files are placed in the `/etc` directory. For example, the first configuration file for Intel® Communications Chipset 8925 to 8955 Series devices is `dh895xcc_qa_dev0.conf` and the first configuration file for the Intel® Communications Chipset 8900 to 8920 Series is `dh89xxcc_qa_dev0.conf`. If more than one device of a given type is present, it will be 'dev1', 'dev2', etc.

The files are processed when the system boots. If changes are made to the configuration file, the Acceleration software must be stopped and restarted for the changes to take effect. Refer to Starting/Stopping the Acceleration Software on page 19 for detailed instructions.

The software package includes multiple types of platform-specific configuration files. Depending on your installation options and SKU, a valid configuration file will be copied to the `/etc` directory for you. If your system has more than one platform, it is recommended that you verify that the correct configuration files were copied.

Note: The software package has been validated with the default configuration files. Changes to the configuration files could have adverse effects.

Refer to the *Programmer's Guide* for your platform for additional information on the configuration files.

3.8 Updating the Acceleration Driver from a Previous Release

If you are upgrading from a previous release of the acceleration driver, you may wish to view the code changes between the releases. Changes would include those made by Intel for the new release as well as user changes to the previous release.

To identify the code changes between a previous release and the current release, perform the following steps:

1. If using the QATmux pkg, then extract this first:

   ```
   #tar xzof QATmux.L.<version>.tar.gz
   ```

   The acceleration packages are then contained in directories QAT1.5 and QAT1.6.

2. Extract the acceleration software package from the new release:

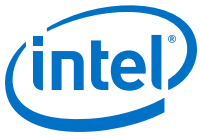
   ```
   # tar xzof QAT*.L.<version>.tar.gz
   ```

3. Execute the `diff` command passing in the path to the previous release quickassist directory and the path to the new release's quickassist directory to capture the output to a file. The command would look like:

   ```
   # diff -x'*.a' -x'*.o' -r  /<prev_version>_QAT/quickassist $ICP_ROOT/
   quickassist > diff.patch
   ```

   where:

   - `/<prev_version>_QAT` indicates the directory where the previous release is located



- `$ICP_ROOT` indicates the directory where the new release is located

The results of the `diff` command are stored in the `diff.patch` file. This will include changes Intel has made between the two releases, as well as any modifications you have made. The `-x` option is used to ignore differences between intermediate files.

Note: When first reviewing the changes between the releases, it may be beneficial to have the tool ignore changes to the Version string and Copyright string. Each source file includes these strings and they will be different between releases. Adding `-I "version:" -I "Copyright"` to the command above will prevent these differences from being included in the diff file. Once you have identified the main changes between the releases, it is strongly advised to include version string updates in your final patch. This ensures that the software is marked with the proper version string.

The created patch can be used as a starting point in updating your previous release to the current release. You will need to review this file to identify the changes you have made to the driver that you wish to keep. For each change you wish to keep, remove the corresponding lines from the patch file. Once this task is completed, you can proceed with applying the patch.

Note: It is very important to review the changes called out in the patch file to identify your changes. If this is not done, your changes to the driver will be lost after applying the patch.

To apply the patch, perform the following steps:

1. Change directories to your previous quickassist directory:

```
# cd /<prev_version>_QAT/quickassist
```

2. Attempt to apply the patch. To avoid any write permission errors, this should be done by the same user who created the original folder that is being patched.

```
# patch -p1 < diff.patch
```

If the patch does not apply cleanly, this suggests that both Intel and user have modified the same sections of code in ways that the patch utility cannot resolve. Refer to the created `.rej` files for additional information on the conflicts.

Note: It is important to compile from the proper directory after applying the patch. In the example above, the compiles must be done in the directory:

```
/<prev_version>_QAT/quickassist
```

not in the directory:

```
$ICP_ROOT/quickassist
```

Once the conflicts (if any) are resolved, you must update the firmware image. This can be done by performing the following:

- Copy the firmware images from the new release to your previous quickassist directory:

```
# cp $ICP_ROOT/quickassist/lookaside/firmware/icp_qat_ae.*
/<prev_version>_QAT/quickassist/lookassist/firmware
```

3.9 Minimizing Acceleration Software Compilation Time

When compiling/installing the Acceleration Software, a "make clean" operation is performed. This results in rebuilding every source file included in the package, even if the source files are unaltered. The "make clean" is done to ensure a proper build is performed. Here are a few items that could cause issues that would require the "make clean":

- If any compile time build flags are added which may not be reflected in the already-built object files.
- If changes are made to header files, performing make clean prior to the make would be preferred.

If you are comfortable with these constraints, you can update the `$ICP_ROOT/quickassist/Makefile` line where ALL_TARGETS is defined and remove the 'clean' from the list of targets. This will remove the clean operation from the build process.

Before the update, the line looks like:

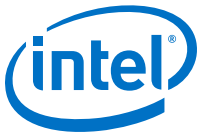
```
ALL_TARGETS = clean lac_lib_dir libosal libosal_user hal adf
adf_user

lac lac_user qat-fw install_scripts
```

After the update, the line looks like:

```
ALL_TARGETS = lac_lib_dir libosal libosal_user hal adf adf_user

lac lac_user qat-fw install_scripts
```



4.0 Sample Applications

This section describes the sample code that can be executed on the target platform along with instructions on their usage.

4.1 QuickAssist Acceleration Sample Application

The software package contains a set of sample tests that exercises acceleration functionality. This section describes the steps required to build and execute the sample tests.

The sample application is provided for both Kernel Space and User Space and the following sections contain instructions for both.

Note: The memory driver included with the sample application is a sample memory driver and is not intended for actual deployment.

4.1.1 Compiling the Acceleration Sample Code

The acceleration sample code can be built from the installation script, or it can be compiled manually.

If the installer was used and all code built successfully, it may be possible to proceed directly to Loading the Sample Code on page 29.

Note: These instructions assume the software package was untarred in the `/QAT` directory and the kernel source files were placed in the directory specified in this guide.

To build from the installation script, do the following:

1. Open a Terminal Window and switch to superuser:

```
# su
<enter root password>
```

   Note: For details on running user space applications as non-root user, please refer to the "Running Applications as Non-Root User" section in the applicable Programmer's Guide (see Product Documentation on page 8).

2. In the `/QAT` directory, start the installation script.

```
# cd /QAT
# ./installer.sh
```

   If the configuration section has built sample code for 1.x then select "build" to build the sample code. If the configuration section does not have sample code, select "`Change Configuration`" to option a1, "`Set Build Target as "sample_code only`", or use command line option "`./installer.sh`

`build_sample [QAT1.5|QAT1.6|mux]` ". This option compiles the Acceleration Sample code for both user space and kernel space. It also compiles the memory mapping driver used with the user space application.

You may be prompted for a directory location to build the package and the Build Output Directory. Use the default value for the location to build the package. The Build Output Directory parameter is ignored.

Note: In the case where both QAT1.5 and QAT1.6 drivers are loaded, the sample code from the QAT1.6 driver should be used.

3. Proceed to signOfLife Tests on page 30 for instructions on executing the tests.

To manually compile the acceleration sample code, do the following:

1. The following environment variables must be set to build the modules:

```
# export ICP_ROOT=<QATdir>
# export ICP_BUILDSYSTEM_PATH=$ICP_ROOT/quickassist/build_system
# export ICP_ENV_DIR=$ICP_ROOT/quickassist/build_system/build_files/env_files
```

   where `<QATdir>` is /QAT or /QAT/QAT1.6 or /QAT/QAT1.5, depending on what hardware support is required.

2. If intermediate modules are required, the following variables must also be set:

```
# export ICP_TOOLS_TARGET=accelcomp
```

3. The sample code is compiled with the default assumption that the kernel source header files are located in one of the following directories:

   64-bit: `/usr/src/kernels/3.1.0-7.fc16.x86_64`

   32-bit: `/usr/src/kernels/3.1.0-7.fc16.i686`

4. If building with mux support, set the mux environment variable:

```
# export WITH_CPA_MUX=1
```

5. If the kernel source header files are located in a different directory, create the environment variable with the directory of desired target kernel sources. For example:

```
# export KERNEL_SOURCE_ROOT=/usr/src/kernels/linux
```

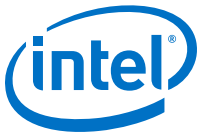
6. You can compile for both Kernel space and User space at the same time using the following commands:

```
# cd $ICP_ROOT/quickassist/lookaside/access_layer/src/sample_code
# make perf_all
```

   The generated Linux kernel object and sample application are located at:

```
$ICP_ROOT/quickassist/lookaside/access_layer/src/sample_code/build
```

Proceed to signOfLife Tests on page 30 for instructions on executing the tests.



4.1.2 Loading the Sample Code

1. The acceleration kernel module must be installed and the software must be started before attempting to execute the sample code. This can be verified by running the following commands:

```
# lsmod | grep "qa"
# service qat_service status
```

Note: The kernel object names may be different depending on the devices installed on the platform. Refer to the table in Section 4.4.2 for kernel object names.

Typical output with two acceleration devices is:

```
There is 2 acceleration device(s) in the system:
 icp_dev0 - type=dh895xcc, inst_id=0, bsf=03:00:0, #accel=6, #engines=12,
state=up
 icp_dev1 - type=dh895xcc, inst_id=1, bsf=82:00:0, #accel=6, #engines=12,
state=up
```

Note: If the module is not returned from the first command, refer to Starting/ Stopping the Acceleration Software on page 19 for additional information on starting the Acceleration software.

2. The sample code is executed by installing the `cpa_sample_code` kernel object for kernel space, or by launching the application for user space.

   The application allows the kernel parameters listed below.

Table 4. Sample Code Parameters

Parameter	Description
configFileVer	Version of configuration file. Can be 1 or 2 (default). If you are using the original version 1 configuration file, use 1. For configuration file details, see the *Programmer's Guide* for your platform.
cyNumBuffers=w	Number of buffers submitted for each iteration. (default=20)
cySymLoops=x	Number of iterations of all symmetric code tests. (default=5000)
cyAsymLoops=y	Number of iterations of all asymmetric code tests. (default=5000)
runTests=1	Run symmetric code tests.
runTests=2	Run RSA test code.
runTests=4	Run DSA test code.
runTests=8	Run ECDSA test code.
runTests=16	Run Diffie-Hellman code tests.
runTests=32	Run compression code tests.
runTests=63	Run all tests. (default)

continued...

Parameter	Description
runStateful=1	Enable stateful compression tests. Applies when compression code tests are run.
signOfLife=1	Indicates shorter test run that verifies the acceleration software is working. This parameter executes a subset of sample tests. Details are included in signOfLife Tests on page 30. (default=0)
wirelessFirmware	Wireless Firmware enabled. Can be 0 (default) or 1. For configuration file details, see the *Programmer's Guide* for your platform.

4.1.2.1 signOfLife Tests

The `signOfLife` parameter is used to specify that a subset of the sample tests are executed with smaller iteration counts. This provides a quick test to verify the acceleration software and hardware are set up correctly.

Note: If the `signOfLife` parameter is not specified, the full run of tests can take several hours to complete. In addition, for RSA 4096 and DH 4096 tests, there can be up to an hour with no perceived result activity.

Kernel Space

After building the sample code, the kernel space kernel driver, the user space application, and the memory mapping driver are located at:

```
$ICP_ROOT/quickassist/lookaside/access_layer/sample_code/build
```

To execute the sign of life test in Kernel space, use the following commands:

```
# export ICP_ROOT=/<QATdir>
# cd $ICP_ROOT/quickassist/lookaside/access_layer/src/sample_code/build
# insmod ./cpa_sample_code.ko signOfLife=1
```

where `<QATdir>` is `/QAT/QAT1.6` or `/QAT/QAT1.5`, depending on what hardware support is required. If building with mux support, always use the QAT1.6 sample code.

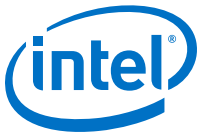
Note: This test takes a few minutes to complete. When the `insmod` command is executed, there is no indication on the terminal window of the activities. Instructions on viewing the results are included in Test Results on page 32.

If loading of the module fails and some messages in `/var/log/messages` show `Device 0 not found or not stated` or `There are no crypto instances`, ensure that the kernel option `intel_iommu` has been configured as specified in Updating Grub Configuration File on page 12, and ensure that the appropriate configuration files have been copied from `$ICP_ROOT/quickassist/config` to `/etc`.

User Space

After building the sample code with the installation script, the kernel space kernel driver, the user space application, and the memory mapping driver are located at:

```
$ICP_ROOT/quickassist/lookaside/access_layer/sample_code/build
```



To execute the sign of life test in User space, use the following commands:

Install the kernel memory driver `qaeMemDrv.ko`, if the module has not already been installed.

```
# insmod $ICP_ROOT/quickassist/lookaside/access_layer/src/sample_code/build/
qaeMemDrv.ko
```

```
# cd $ICP_ROOT/quickassist/lookaside/access_layer/src/sample_code/build
# ./cpa_sample_code signOfLife=1
```

Note: You will observe that execution time of the user space code takes longer than the kernel space code. This is due to the sample code kernel space memory management driver (`qaeMemDrv.ko`), which is slow to allocate and map memory to user space. Before beginning performance measurements, the sample code allocates memory up-front which slows execution time. This does not affect the performance of the acceleration driver itself. The acceleration driver user space and kernel space performance are equivalent, other things being equal (for instance, no throttling takes place in either case).

4.1.2.2 Wireless Tests

The software package includes a version of the firmware optimized for small cryptography packets. In order to run the sample code with this firmware, the following steps must be performed.

1. An example configuration file is included in the package in the `$ICP_ROOT/quickassist/config` folder. For instance, for the Intel® Communications Chipset 8925 to 8955 Series:

   ```
   # cp  $ICP_ROOT/quickassist/config/dh895xcc_qa_dev0.conf.v2.wireless /etc/
   dh895xcc_qa_dev0.conf
   ```

   Note: The commands above apply to device dev0. The same commands should be issued for device dev1.

2. Restart the acceleration service using the command:

   ```
   # service qat_service restart
   ```

3. Run the sample code using the command:

   ```
   # cd $ICP_ROOT/quickassist/lookaside/access_layer/src/sample_code/build
   # ./cpa_sample_code wirelessFirmware=1
   ```

   Note: The `wirelessFirmware=1` parameter must be specified when the wireless config file is used. The following error messages are displayed if the parameter is not specified.

   ```
   There are no crypto instances available
   setupSymmetricTest():1135 Failed to start Crypto services
   main():636 Error calling setupCipherTest
   ```

4.1.3 Test Results

When running the application in kernel space, open a second terminal window, log in as root, and issue the following command:

```
# tail -f /var/log/messages
```

When running the application in user space, the results are printed to the terminal window in which the application is launched.

Example

Here is an example of the log messages created during the test:

```
---------------------------------------
Algorithm Chaining - AES256-CBC HMAC-SHA512
Number of threads    2
Total Submissions    20
Total Responses      20
Packet Size          512
---------------------------------------
```

A similar pattern is repeated for each of the tests.

4.1.4 Unloading the Sample Code

Once the kernel space sample code test has completed, the message `Sample Code Complete` is displayed. The module can then be unloaded using the following command:

```
# rmmod cpa_sample_code.ko
```

Once the user space sample code test has completed, the kernel memory driver `qaeMemDrv.ko` can be unloaded using the following command:

```
# rmmod qaeMemDrv.ko
```

4.2 Intel® QuickAssist API Sample Code

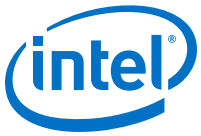
The software package contains sample code that demonstrates how to use the Intel® QuickAssist APIs and build the structures required for various use cases.

For more details, refer to the *Intel® QuickAssist Technology API Programmer's Guide* (see listing in Table 1 on page 8).

4.3 Acceleration Functional Sample Code

The software package contains a set of sample tests that exercises acceleration functionality. This section describes the steps required to build and execute the sample tests.

The sample application is provided for both Kernel Space and User Space and the following sections contain instructions for both.



Note: The memory driver included with the sample application is a sample memory driver and is not intended for actual deployment.

4.3.1 Compiling the Acceleration Functional Sample Code

The acceleration functional sample code can be compiled manually.

Note: These instructions assume the software package has been untarred to the `/QAT` directory and that the kernel source files were placed in the directory specified in this guide.

1. The following environment variable must be set to build the modules:

   ```
   export ICP_ROOT=<QATdir>
   ```

   where `<QATdir>` is `/QAT/QAT1.6` or `/QAT/QAT1.5`, depending on what hardware support is required. If building with mux support, always use the QAT1.6 sample code.
2. The sample code is compiled with the default assumption that the kernel source header files are located in one of the following directories:
   - For 64-bit: `/usr/src/kernels/3.1.0-7.fc16.x86_64`
   - For 32-bit: `/usr/src/kernels/3.1.0-7.fc16.i686`
3. If the kernel source header files are located in a different directory, create the environment variable with the directory of desired target kernel sources. For example:

   ```
   # export KERNEL_SOURCE_ROOT=/usr/src/kernels/`uname -r`
   ```
4. If building with mux support, set the mux environment variable:

   ```
   # export WITH_CPA_MUX=1
   ```
5. You can compile for both Kernel space and User space at the same time using the following commands:

   ```
   # cd $ICP_ROOT/quickassist/lookaside/access_layer/src/sample_code/functional
   # make all
   ```

Result: The generated Linux kernel objects and sample applications are located at: `$ICP_ROOT/quickassist/lookaside/access_layer/src/sample_code/functional/build`

4.3.2 Executing the Acceleration Functional Sample Code in Kernel Space

To execute the acceleration functional sample code in Kernel space, enter the commands:

```
# export ICP_ROOT=<QATdir>
# cd $ICP_ROOT/quickassist/lookaside/access_layer/src/sample_code/functional/build
# insmod ./nrbg_sample.ko
```

Note: `nrbg_sample.ko` is one of the functional kernel modules. You can launch the other `.ko` modules in a similar fashion.

Note: You may observe an error message similar to the following when submitting the `insmod` command:

```
insmod: error inserting '<modulename>': -1 Resource temporarily unavailable
```

This error can be safely ignored. When the test application is completed, the kernel object is removed which causes this error. Test results for the application are available in `/var/log/messages`.

4.3.3 Executing the Acceleration Functional Sample Code in User Space

To execute the acceleration functional sample code in User Space, the kernel memory driver `qaeMemDrv.ko` must be installed. See QuickAssist Acceleration Sample Application on page 27 for information on compiling the performance sample code. The `qaeMemDrv.ko` kernel object is built as part of that sample code.

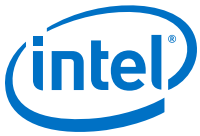
1. Install the kernel memory driver `qaeMemDrv.ko` as follows:

   ```
   # insmod $ICP_ROOT/quickassist/lookaside/access_layer/src/sample_code/build/
   qaeMemDrv.ko
   ```

2. To execute the acceleration functional sample code in user space, use the following commands:

   ```
   #cd $ICP_ROOT/quickassist/lookaside/access_layer/src/sample_code/functional/
   build
   #./nrbg_sample
   ```

   Note: `nrbg_sample` is one of the functional user space applications. You can launch the other user space applications in a similar fashion.



Appendix A Glossary

AHCI	Advanced Host Controller Interface
API	Application Programming Interface
BIOS	Basic Input/Output System
BOM	Bill of Materials
CMOS	Complementary Metal-Oxide Semiconductor
CRB	Customer Reference Board
CY	Cryptography
DC	Data Compression
GUI	Graphical User Interface
GRUB	GRand Unified Bootloader
IDE	Intelligent Drive Electronics
MUX	Multiplex(er)
OS	Operating System
OSAL	Operating System Access Layer
PCH	Platform Controller Hub
PCI	Peripheral Component Interconnect
QAT	Intel® QuickAssist Technology
SATA	Serial Advanced Technology Attachment
Shumway	Codename for Shumway with Intel® Communications Chipset 8925 to 8955 Series
SR-IOV	Single Root-I/O Virtualization